



75622P006501

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Yan Zhou

Application No: 10/749,875

Filed: December 31, 2003

For: SUBSCRIBER LINE INTERFACE
CIRCUITRY TRANSCEIVER

Examiner: Singh, Ramnandan

Group Art Unit: 2646

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William D. Davis

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is submitted under one of the following (as indicated by the "X"):

- ☐ 37 C.F.R. §1.97(b).
- ☒ 37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:
- ☐ A certification pursuant to 37 C.F.R. §1.97(e) or
- ☒ A check for \$180 for the fee under 37 C.F.R. § 1.17(p).
- ☐ 37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:
- (1) A certification pursuant to 37 C.F.R. §1.97(e);
- (2) A petition requesting consideration of the Information Disclosure Statement; and
- (3) A check for \$_____ for the fee under 37 C.F.R. §1.17(i) for submission of the Information Disclosure Statement.
- ☐ 37 C.F.R. §1.97(i). This information disclosure statement is submitted prior to issuance of a patent and applicant requests this statement, the information disclosure citation form, and any accompanying references to be placed in the file pursuant to 37 C.F.R. § 1.97(i).

Enclosed is a copy of Information Disclosure Citation Form, submitted pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, together with copies of any documents cited on that form other than U.S. patent documents. Applicant respectfully requests that the cited documents be considered and that the enclosed Information Disclosure Citation Form be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

If there are any issues that can be resolved by telephone conference, the Examiner is respectfully requested to contact the undersigned at (512) 858-9910.

Respectfully submitted,

Date January 27, 2006

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Application Number	10/749,875
Filing Date	December 31, 2003
First Named Inventor	Yan Zhou
Group Art Unit	2646
Examiner Name	Singh, Ramnandan P.
Attorney Docket Number	75622P006501

Sheet	1	of	2
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[illegible][illegible]Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

⁶Applicant is to place a check mark here if English language Translation is attached.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				Application Number	10/749,875
				Filing Date	December 31, 2003
				First Named Inventor	Yan Zhou
				Group Art Unit	2646
				Examiner Name	Singh, Ramnandan P.
Sheet	2	of	2	Attorney Docket Number	75622P006501

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX Global Enhanced Multiport Integrated ADSL Transceiver", Infineon Technologies AG, Germany (2001)	
		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX ADSL Transceiver Chipset", Infineon Technologies AG, Germany (2001)	
		INFINEON TECHNOLOGIES, "Press Release: Infineon Technologies Introduces the IVAX Family - the First Chipset to Integrate Voice and Fullrate ADSL on a Single Line Card", Infineon Technologies AG, Gernay (May 15, 2001)	
		PESSL, PETER, ET AL. "A four channel Integrated Voice and ADSL Full-Rate Analog Front End", 27th European Solid-State Circuits Conference, Villach, Austria, September 18-20, 2001	

Examiner Signature		Date Considered	
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